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... ADDRESS. Do not set within a **flash** memory block used by ; the ROM Monitor. ;—
VECTOR_ADR ...

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... KNC30WA (2) Control software: KD30 (3) Target board **Flash** Memory programming ... vector
table Vvector .equ 01FFDCh ; top address of **fixed vector table** SB_base .equ ...

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... dresses for those interrupt service routines should be set in the **fixed vector table**. ... The
address match interrupt cannot be used because the **flash** memory's ...

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... 8KB Bild 1-2: Zusammenstellung der gängigsten M16C62 Controller mit **Flash**-Memory. ... RAM
18.6KB Monitor Program RAM 1.4KB User **Fixed Vector Table** Monitor Program ...

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... 4.0 to 5.5V (f(X IN)=10MHz) :mask ROM version 2.7 to 5.5V (f(X IN)=3.5MHz
) :mask ROM version 4.0 to 5.5V (f(X IN)=10MHz) :**flash** memory version ...

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... discrete devices, and memory chips other than DRAMs (**flash** memory, SRAMs ... vector table
Vvector .equ 0FFFDCh ; top address of **fixed vector table** ;— keep of ...

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... Figure 6-1. MSA0654G01/G02's memory map SFR RAM 20KB Flash memory 256KB ... KB User fixed vector area Monitor program 16KB FC000h User **fixed vector table** 512 Byte ...

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